In the Claims

Kindly cancel claims 1-47.

Kindly amend claims 48-53 and add claims 54-63 as follows.

48. (Amended) An energy-conserving microprocessor or CPU system comprising:

(a) keep-alive circuitry operable for performing auxiliary information processing when receiving keep-alive power; and

(b) main circuitry operable for performing main information processing only when receiving main power.

49. (Amended) The energy-conserving microprocessor or CPU system of claim 48, wherein said keep-alive circuitry is adapted to be also operable when said main power is present.

50. (Amended) The energy-conserving microprocessor or CPU system of claim 48, wherein said keep-alive circuitry is provided for controlling an activity of associated device means when said main power is absent.

51. (Amended) The energy-conserving microprocessor or CPU system of claim 48, wherein said keep-alive circuitry is provided for performing a keep-alive task when said main power is absent, said keep-alive task including to actuate said main circuitry when needed.

52. (Amended) The energy-conserving microprocessor or CPU system of claim 48, wherein said keep-alive circuitry is adapted to establish circuit communication with an interfacing means provided for transmitting a signal issued from an external means so as to request said keep-alive circuitry to perform a requested activity selectively when said keep-alive power or said main power is present.

53. (Amended) The energy-conserving microprocessor or CPU system of claim 48, wherein said main circuitry is adapted to establish circuit communication with an interfacing means provided for transmitting a signal issued from an external means so as to request said main circuitry to perform a requested activity when said main power is present.

54. (New) The energy-conserving microprocessor or CPU system of claim 48, wherein said keep-alive circuitry is adapted to be de-actuatable in response to a request signal.

55. (New) The energy-conserving microprocessor or CPU system of claim 48 further comprising means operable for cooling said main circuitry only when said main power is present.

56. (New) An energy-conserving method for a microprocessor or CPU system comprising:

(a) providing auxiliary information processing when receiving auxiliary power; and

(b) actuating main information processing only when receiving main power.

57. (New) The energy-conserving method for a microprocessor or CPU system of claim 56, wherein said providing auxiliary information processing is also provided when said main power is present.

58. (New) The energy-conserving method for a microprocessor or CPU system of claim 56, wherein said providing auxiliary information processing is provided for controlling an activity of associated device means when said main power is absent.

59. (New) The energy-conserving method for a microprocessor or CPU system of claim 56, wherein said providing auxiliary information method is provided for performing a keep-alive task including to actuate said actuating main information processing when needed.

60. (New) The energy-conserving method for a microprocessor or CPU system of claim 56, wherein said providing auxiliary information processing is provided in response to a request signal selectively when said auxiliary power or said main power is present.

61. (New) The energy-conserving method for a microprocessor or CPU system of claim 56, wherein said actuating main information processing is provided in response to a request signal.

62. (New) The energy-conserving method for a microprocessor or CPU system of claim 56 further comprising a step of de-actuating said providing auxiliary information processing in response to a request signal.

63. (New) The energy-conserving method for a microprocessor or CPU system of claim 56 further comprising a step of actuating cooling only when said actuating main information processing is actuated.